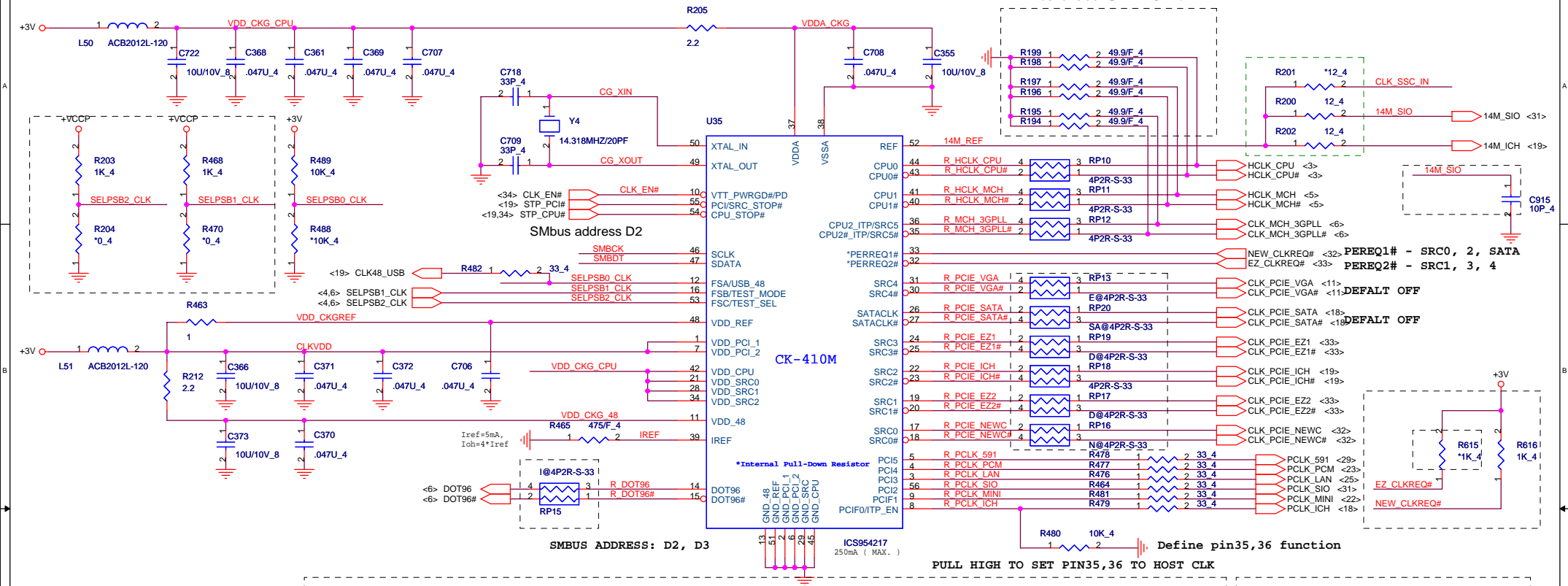


Place these termination
to close CK410M.



PULL HIGH TO SET PIN35,36 TO HOST CLK

Place these termination
to close CK410M.

FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

QUANTA
COMPUTER

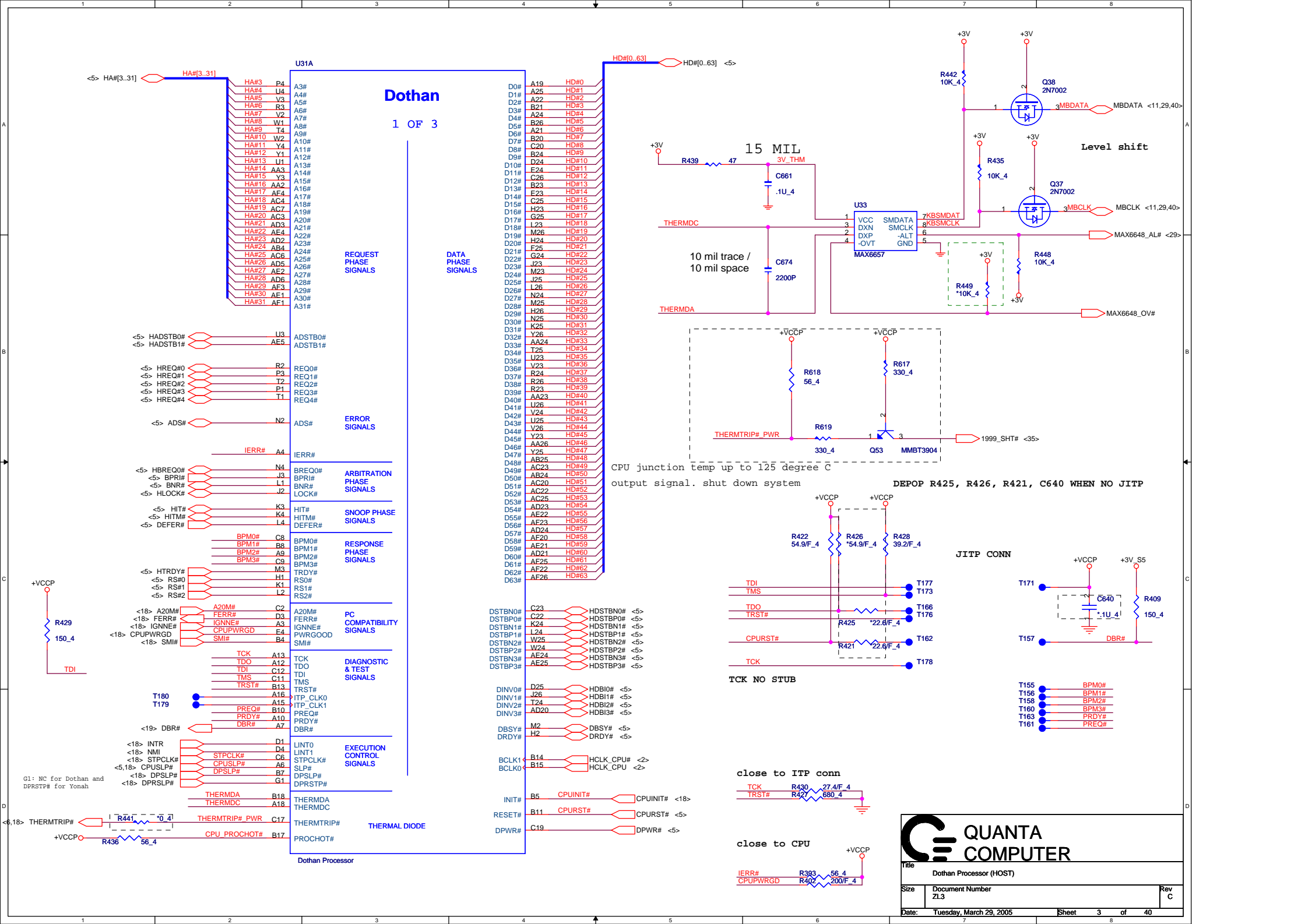
CLOCK GENERATOR

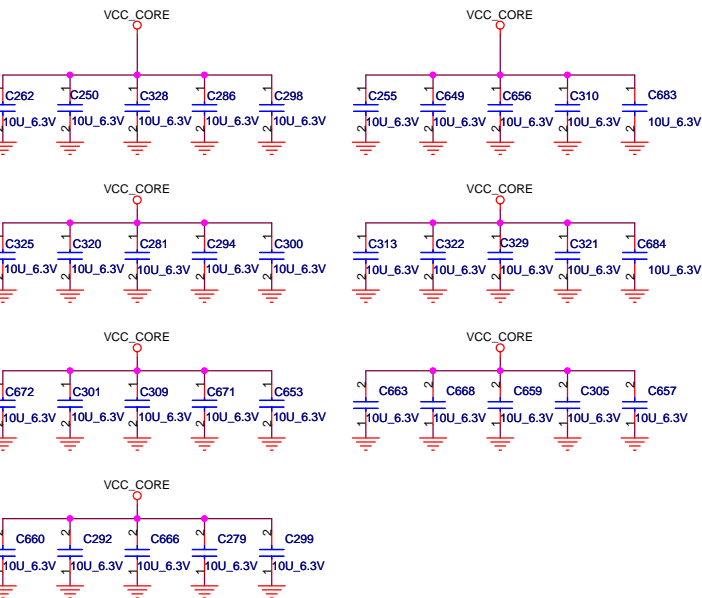
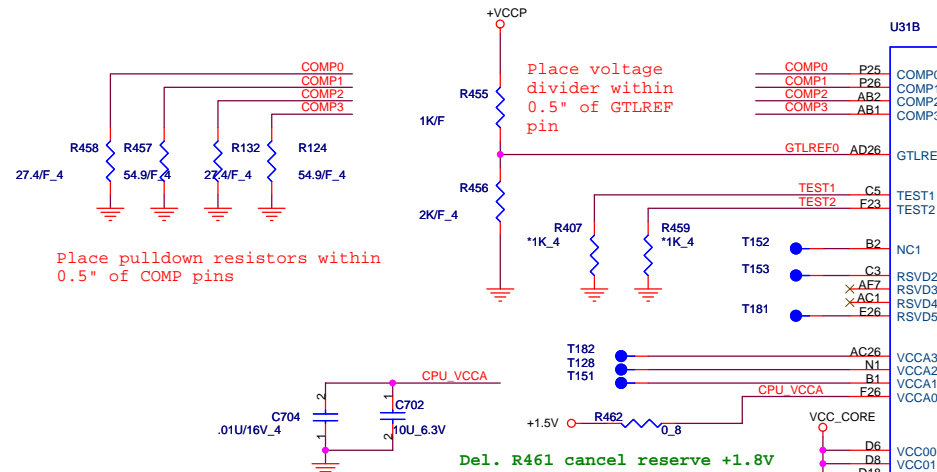
Size Document Number
ZL3

Date: Tuesday, March 29, 2005

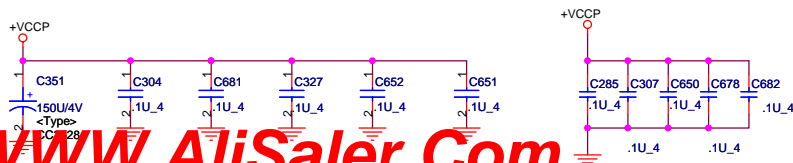
Sheet 2 of 40

Rev C





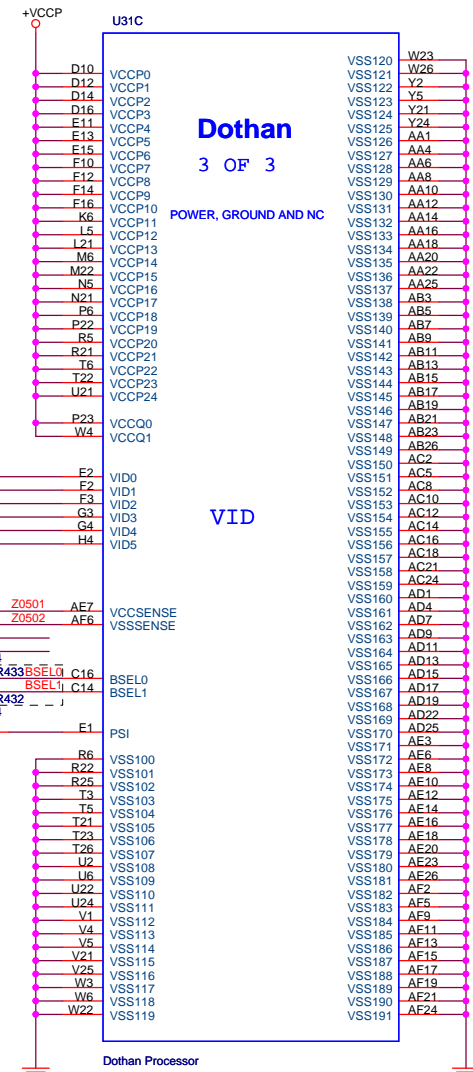
Total caps = 2633 uF
ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15



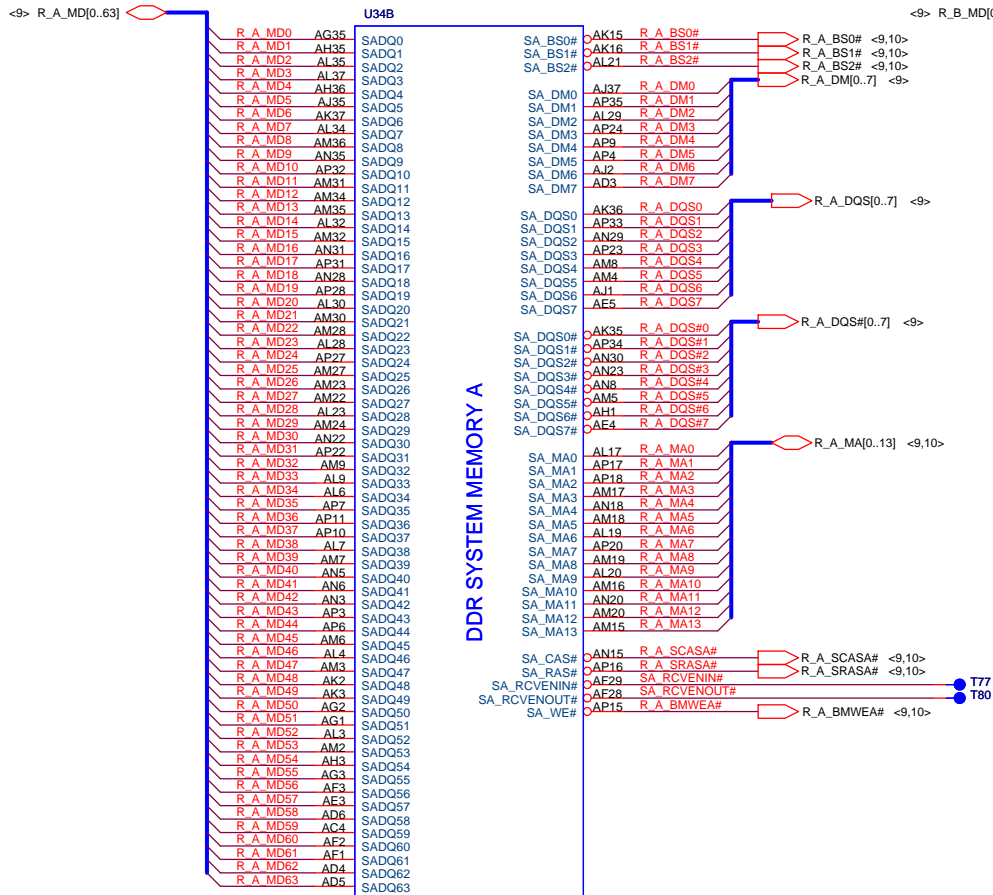
Dothan
2 OF 3

POWER, GROUND, RESERVED SIGNALS

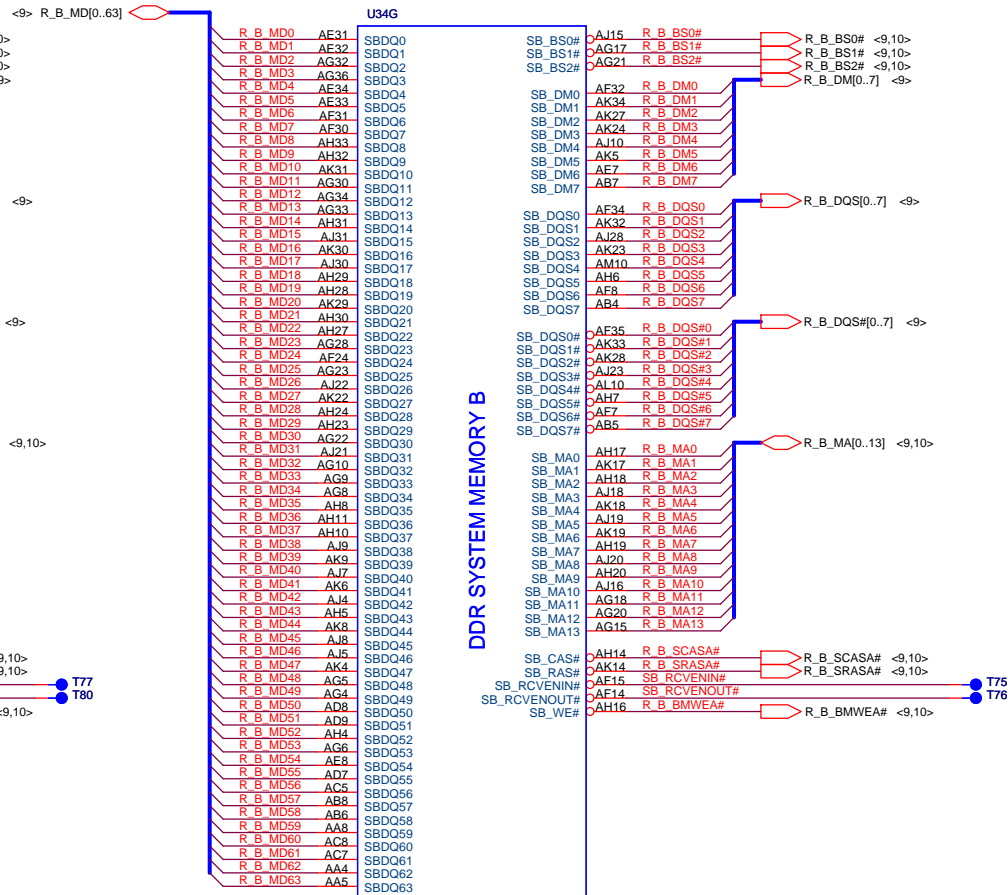
Dothan Processor



USME		
AF23	VSS136	AG37
H23	VSS137	Y37
AL22	VSS138	V37
AH22	VSS139	T37
J22	VSS140	P37
E22	VSS141	M37
D22	VSS142	K37
A22	VSS143	H37
AN21	VSS144	E37
AF21	VSS145	AN36
F21	VSS146	AL36
C21	VSS147	AJ36
AK20	VSS148	AF36
V20	VSS149	AE36
G20	VSS150	AD36
F20	VSS151	AC36
E20	VSS152	AB36
D20	VSS153	AA36
A20	VSS154	C36
AN19	VSS155	AE35
AG19	VSS156	Y35
W19	VSS157	W35
T19	VSS158	V35
J19	VSS159	U35
H19	VSS160	T35
C19	VSS161	R35
AL18	VSS162	P35
U18	VSS163	N35
B18	VSS164	M35
A18	VSS165	L35
AN17	VSS166	K35
AF17	VSS167	J35
G17	VSS168	I35
C17	VSS169	H35
AL16	VSS170	G35
K16	VSS171	F35
H16	VSS172	E35
D16	VSS173	D35
A16	VSS174	C35
C15	VSS175	B35
K15	VSS176	AN34
C15	VSS177	AH34
AN14	VSS178	VSS39
AL14	VSS179	AD34
AG14	VSS180	AC34
K14	VSS181	AB34
J14	VSS182	AA34
B14	VSS183	PA34
A14	VSS184	AL33
D12	VSS185	AD33
B12	VSS186	AD33
J12	VSS187	U33
D12	VSS188	U33
B12	VSS189	T33
AN11	VSS190	P33
AL11	VSS191	N33
AG11	VSS192	M33
AF11	VSS193	L33
AA11	VSS194	K33
Y11	VSS195	J33
H11	VSS196	I33
F11	VSS197	H33
AA10	VSS198	G33
Y10	VSS199	F33
L10	VSS200	E33
D10	VSS201	D33
AN9	VSS202	AN32
AH9	VSS203	AJ32
AE9	VSS204	AD32
AC9	VSS205	AC32
AA9	VSS206	AB32
Y9	VSS207	AA32
T9	VSS208	Y32
K9	VSS209	W32
A9	VSS210	V32
AL8	VSS211	U32
Y8	VSS212	T32
P8	VSS213	R32
L8	VSS214	P32
CR	VSS215	N32
AK7	VSS216	M32
AG7	VSS217	L32
AK7	VSS218	K32
AG7	VSS219	J32
Y7	VSS220	I32
G7	VSS221	H32
AL6	VSS222	G32
AE6	VSS223	F32
AC6	VSS224	E32
AA6	VSS225	D32
T6	VSS226	C32
P6	VSS227	B32
L6	VSS228	A32
BE	VSS229	Y30
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AL5	VSS231	V29
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AC3	VSS243	A29
AB3	VSS244	Y28
AA3	VSS245	W28
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AE2	VSS249	P28
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@ALVISO_GM/GML



@ALVISO_GM/GML



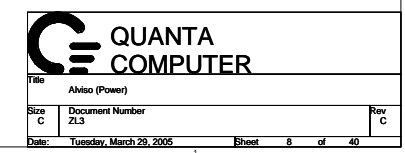
Alviso (DDR)

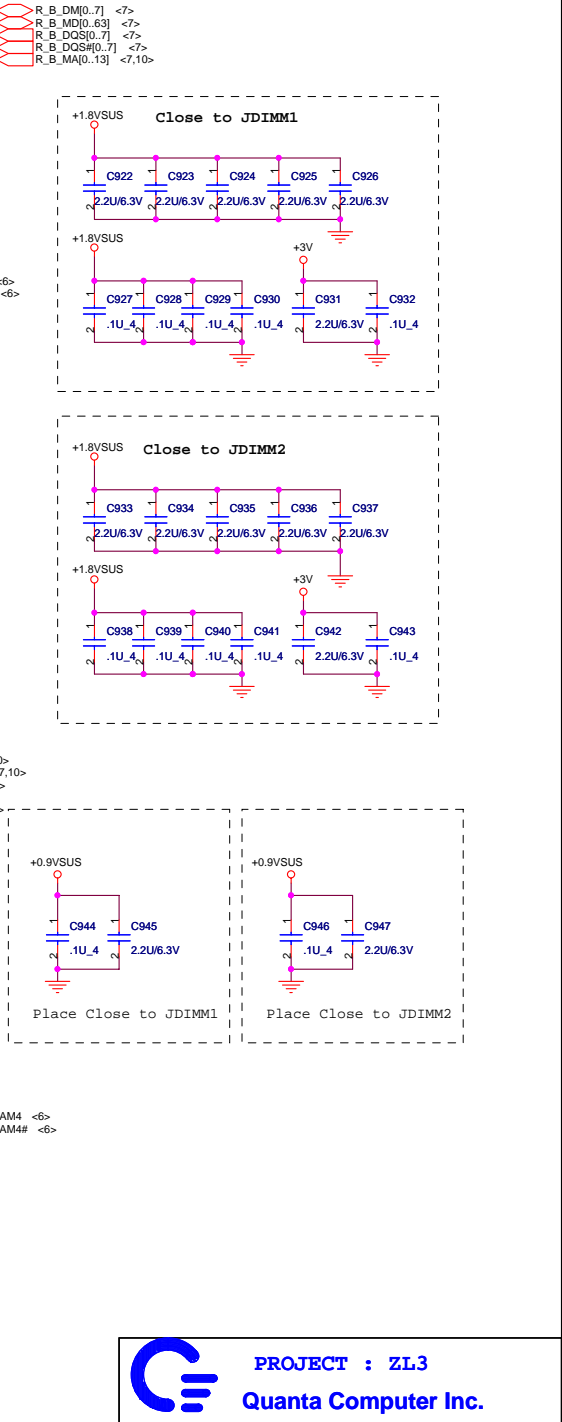
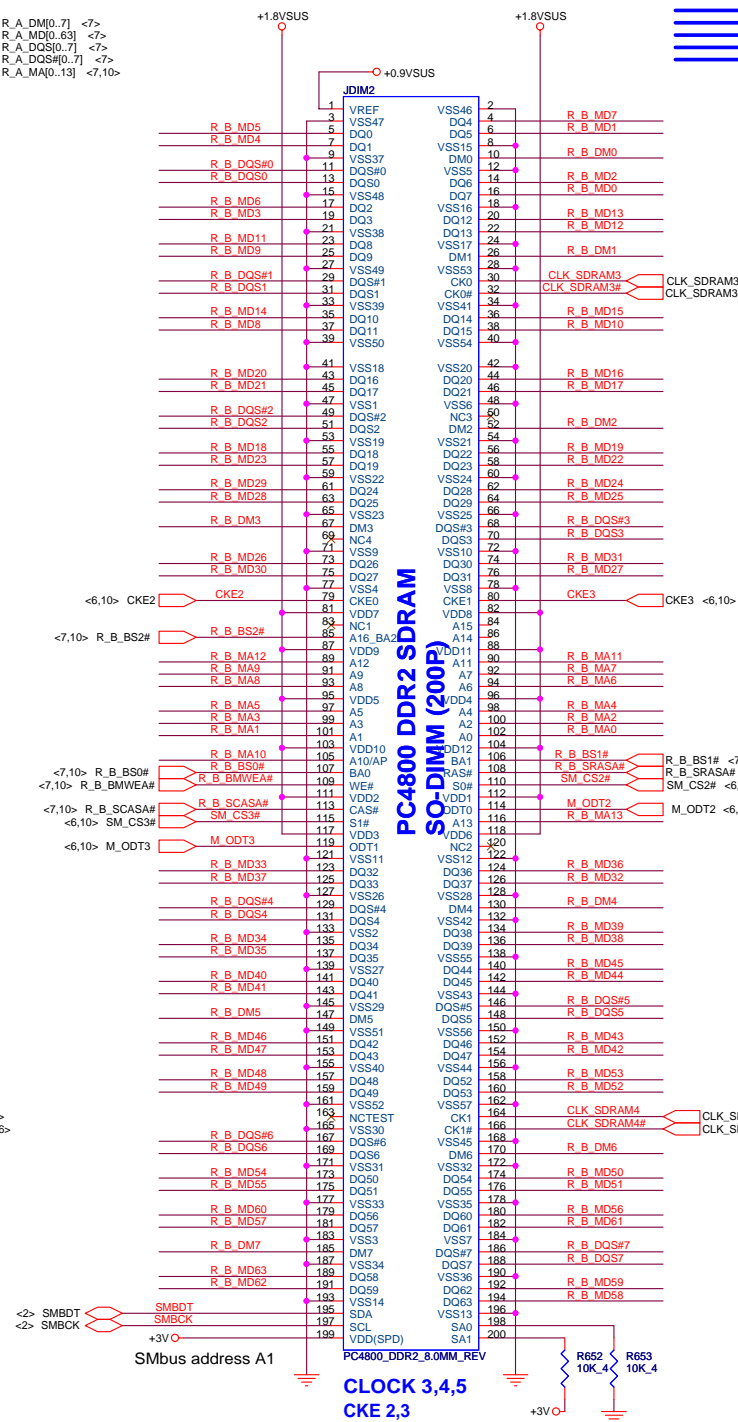
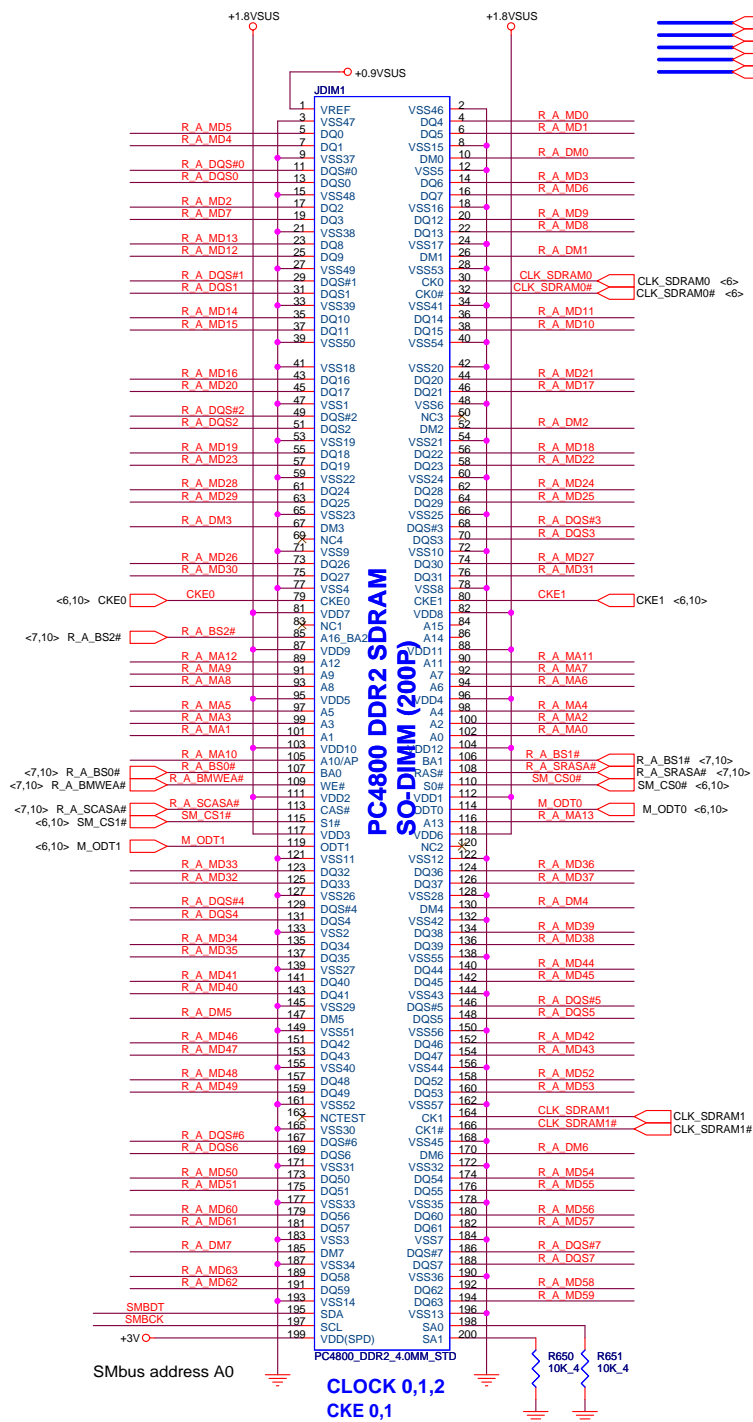
Size Document Number


CustomZL3

Date: Tuesday, March 29, 2005 Sheet 7 of 40

Rev C



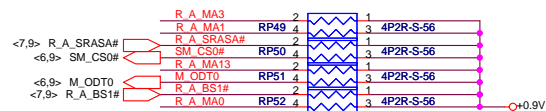


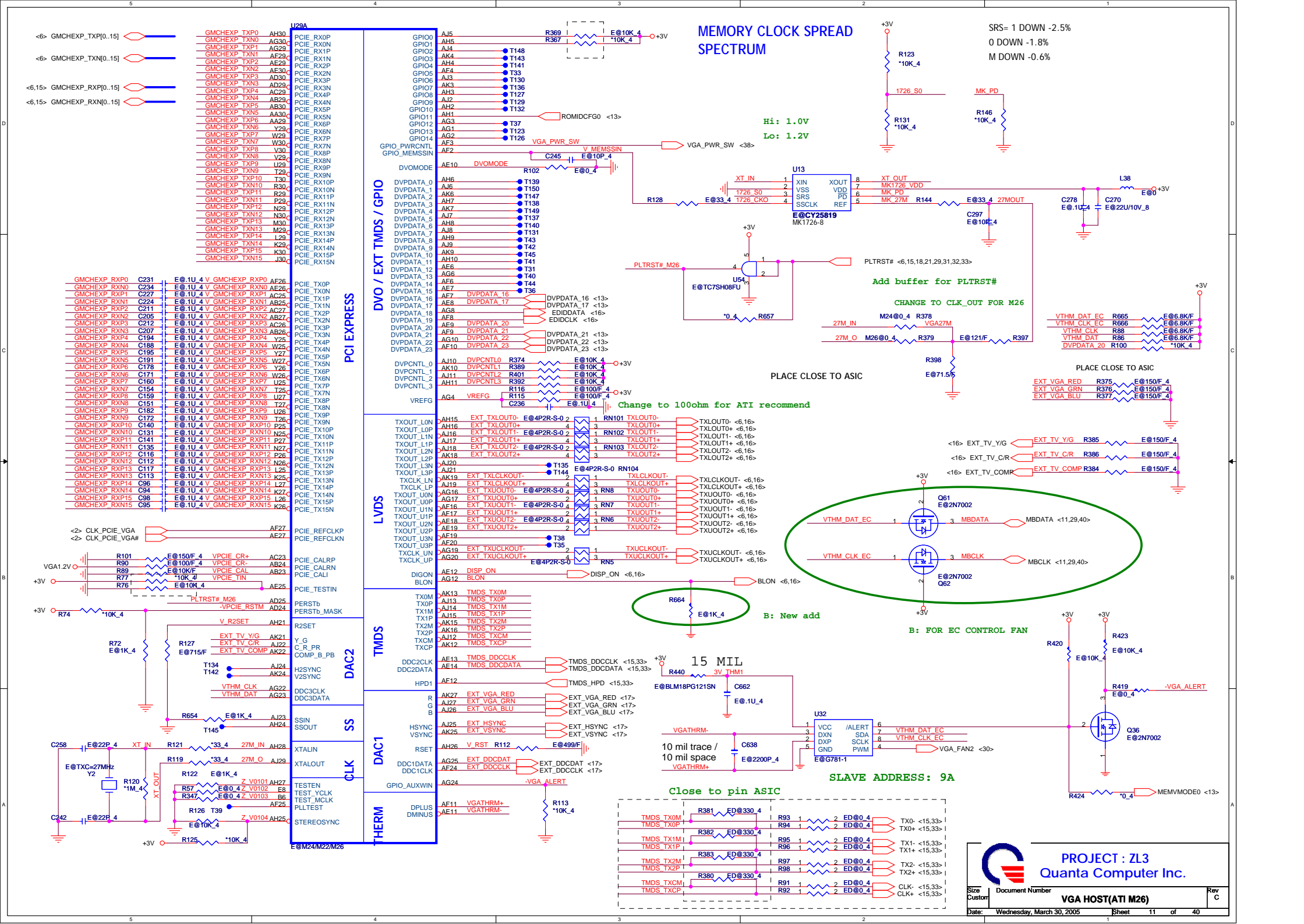


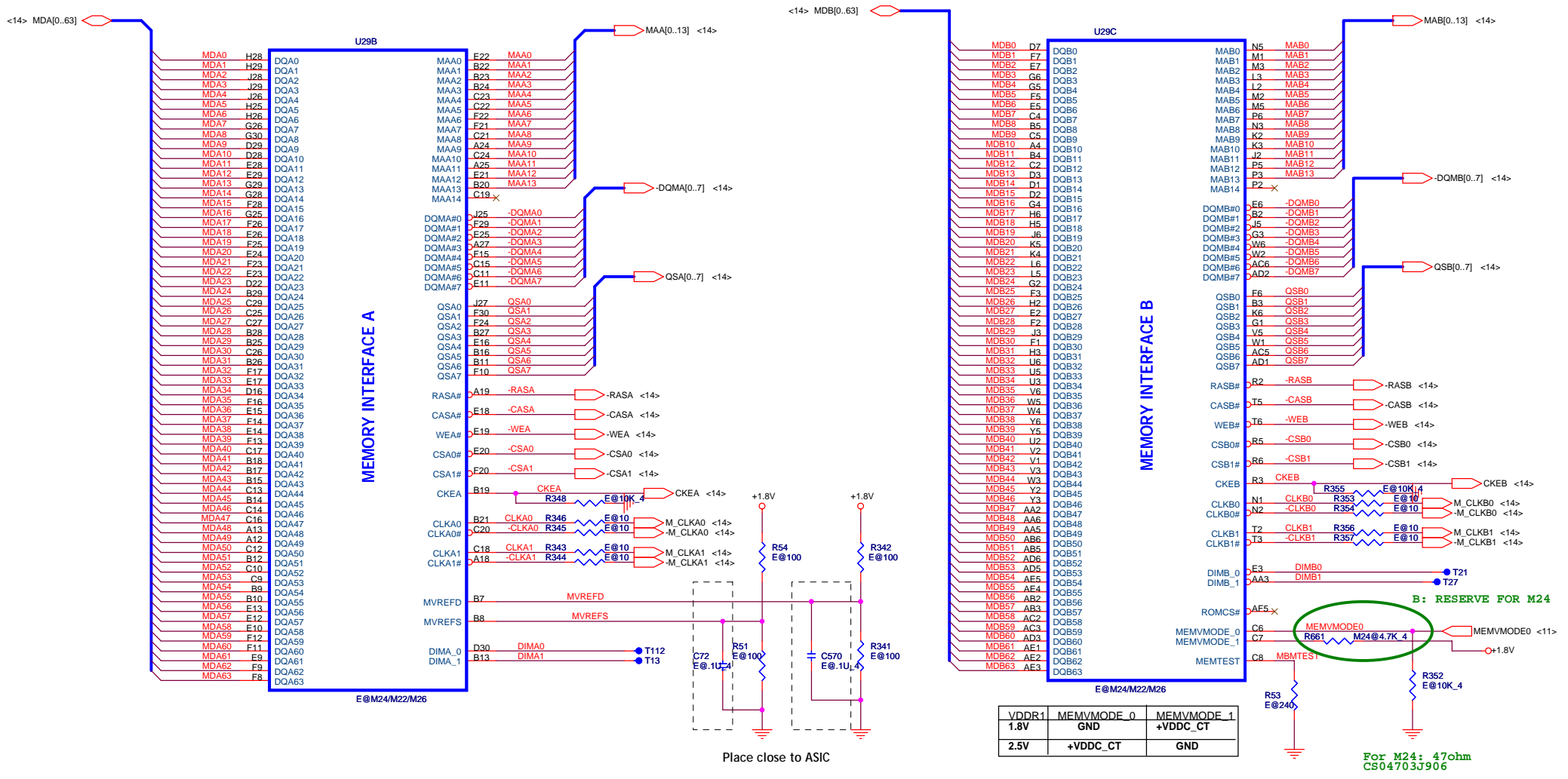
PROJECT : ZL3

Quanta Computer Inc.

Size	Document Number	Rev
	DDR2 SO-DIMM (200P)	C
Date:	Tuesday, March 29, 2005	Sheet 9 of 40

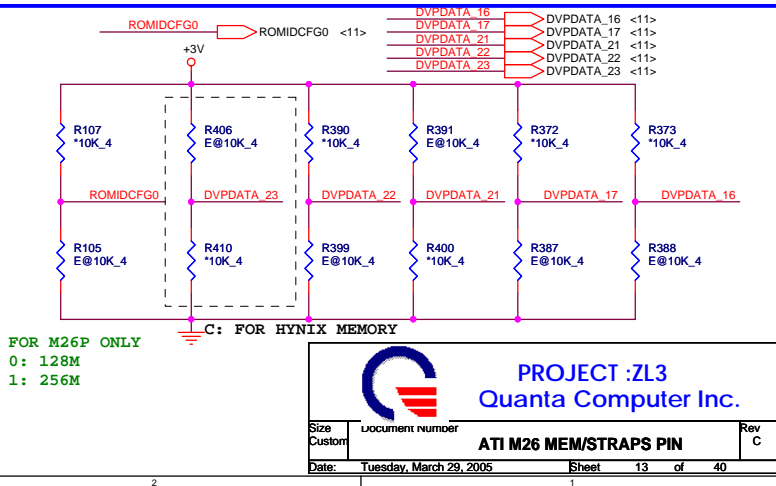







GPIO_0	PCI-Express Current Calibration Bandgap Backup
	0: use reference voltage from Bandgap 1: use reference voltage from resistor divider
GPIO_1	PCI-Express PLL Calibration force enable
	0: Disable PLL force calibration 1: Enable PLL force calibration
GPIO_(3,2)	00: PCI Express 1.0 mode
	01: RESERVED
	10: PCI Express 1.0 mode
	11: RESERVED
GPIO_4	Turn off PCI-Express impedance / strength calibration
	0: enable 1: disable
GPIO_5	Bypass PCI-Express PLL

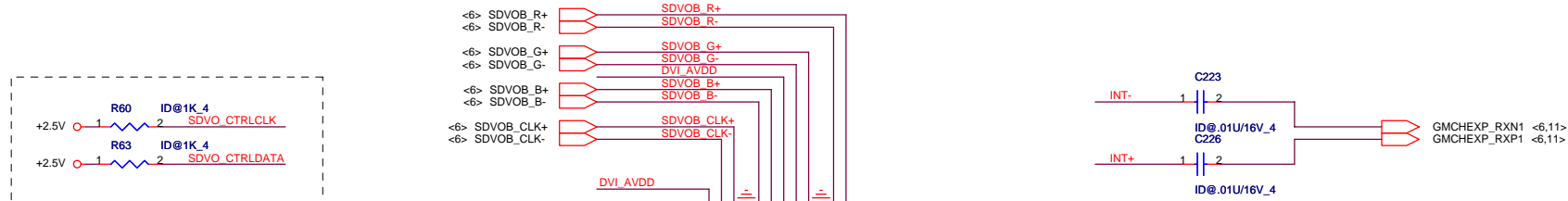
GPIO_6	PCI-Express transmitter current compensation
	0: Normal 1: Inject extra current for output buffer switching
GPIO_8	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible
	ROMIDCFG
GPIO(9,13:11) INT P/D	0x0x: No ROM, CHG_ID=0
	0x1x: No Rom, CHG_ID=1
	1000: Parallel ROM, Chip ID'S from ROM
	1000: Parallel ROM, Chip ID'S from ROM
DVPDATA_21-23 MEM TYPE	DVPDATA_21: 0=4Mx32 1=8Mx32
	DVPDATA_22: 0=128M 1=64M
	DVPDATA_23: 0=Hynix 1=Samsung





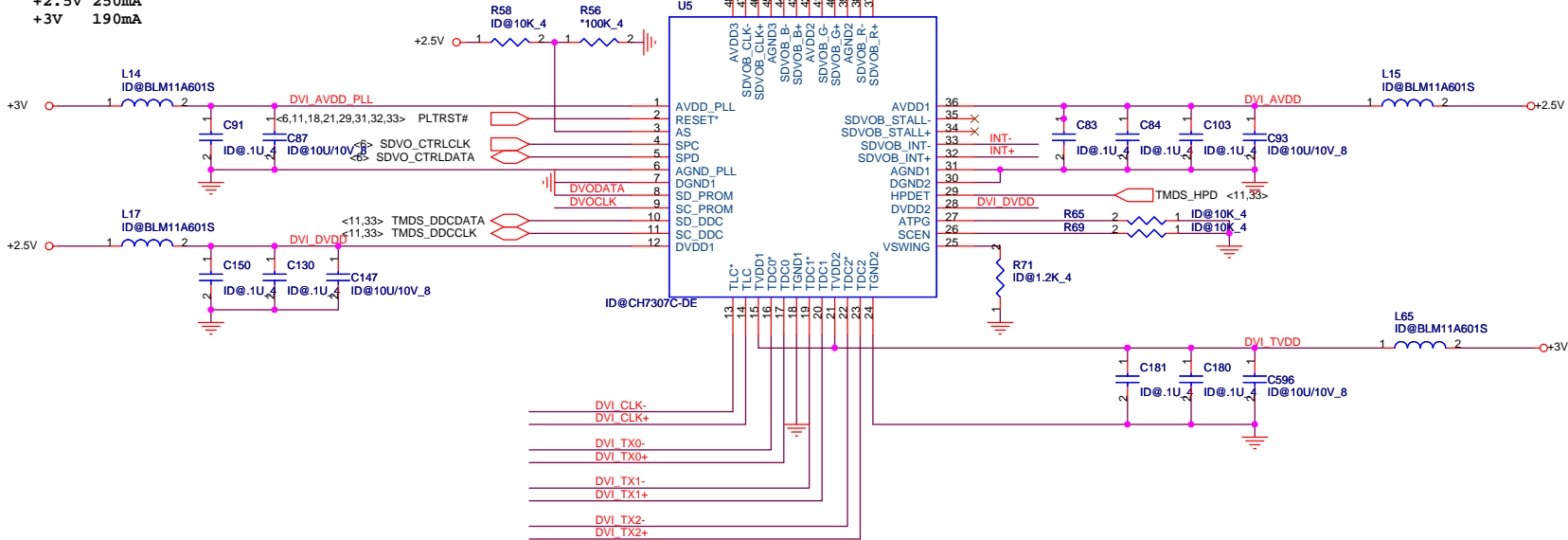
PROJECT :ZL3
Quanta Computer Inc.

Size	Document number	ATI M26 MEM/STRAPS PIN	Rev
Custom			C
Date:	Tuesday, March 29, 2005	Sheet	13 of 40

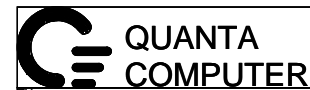
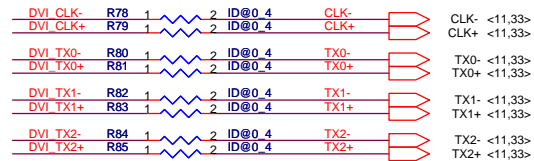
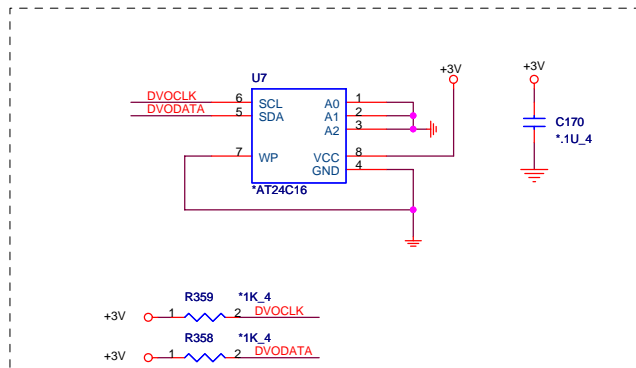


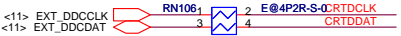
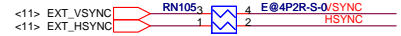
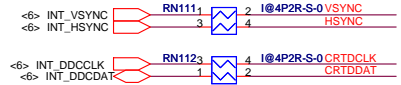
PULL LOW FOR DVO NOT PRESENT (INTERNAL PULLDOWN IN 915GM)

+2.5V 250mA
+3V 1.90mA

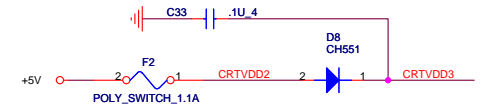
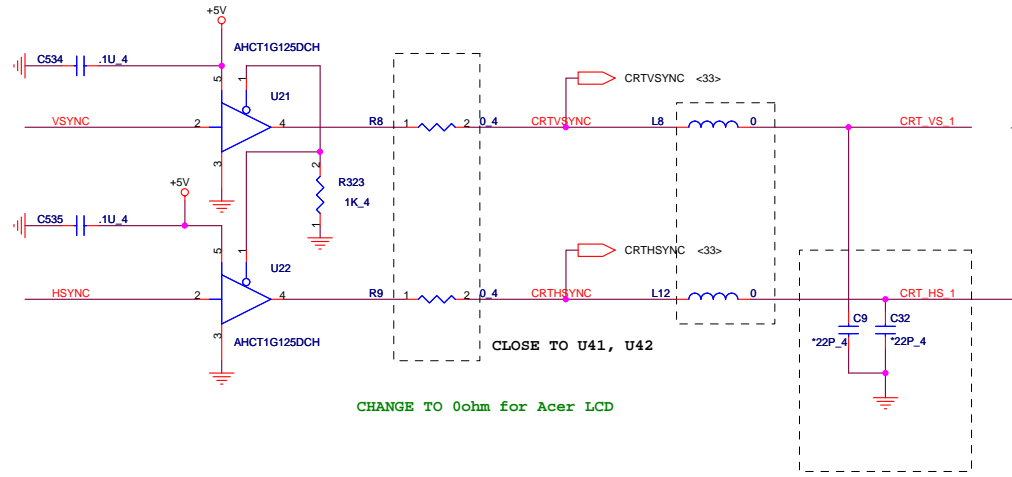
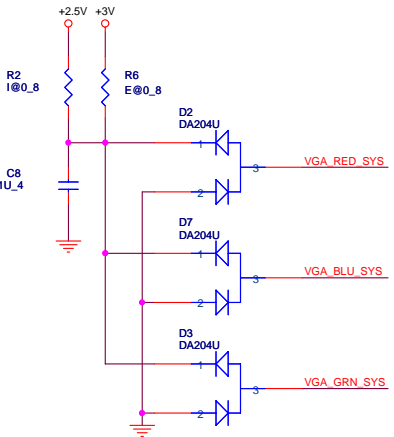
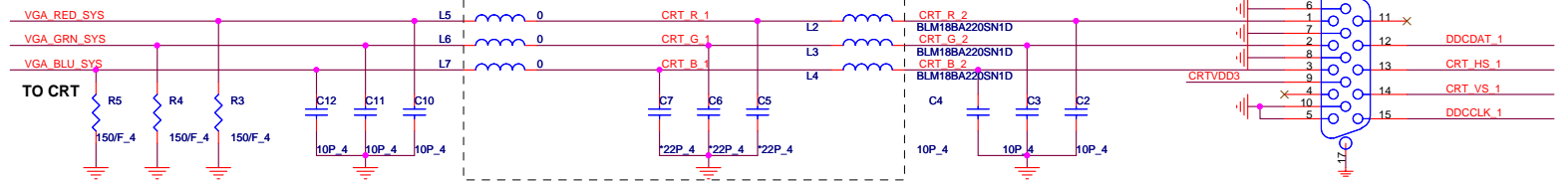
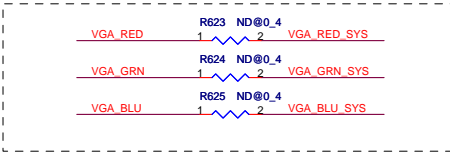
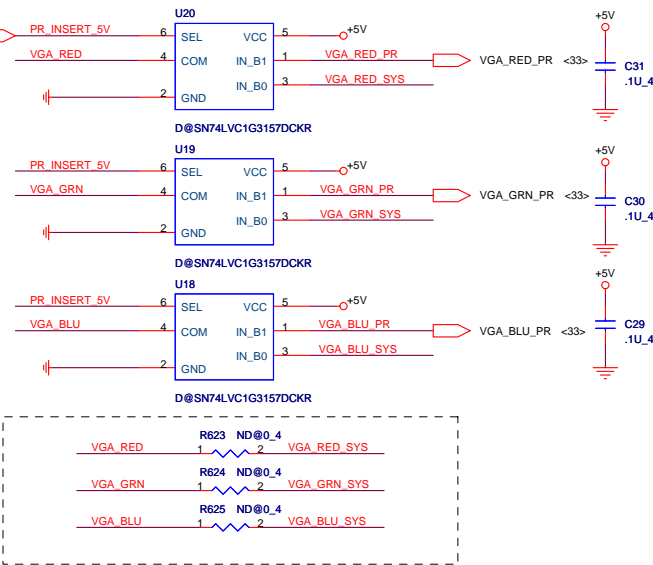


ALWAYS NOT ON, TEST ONLY





SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

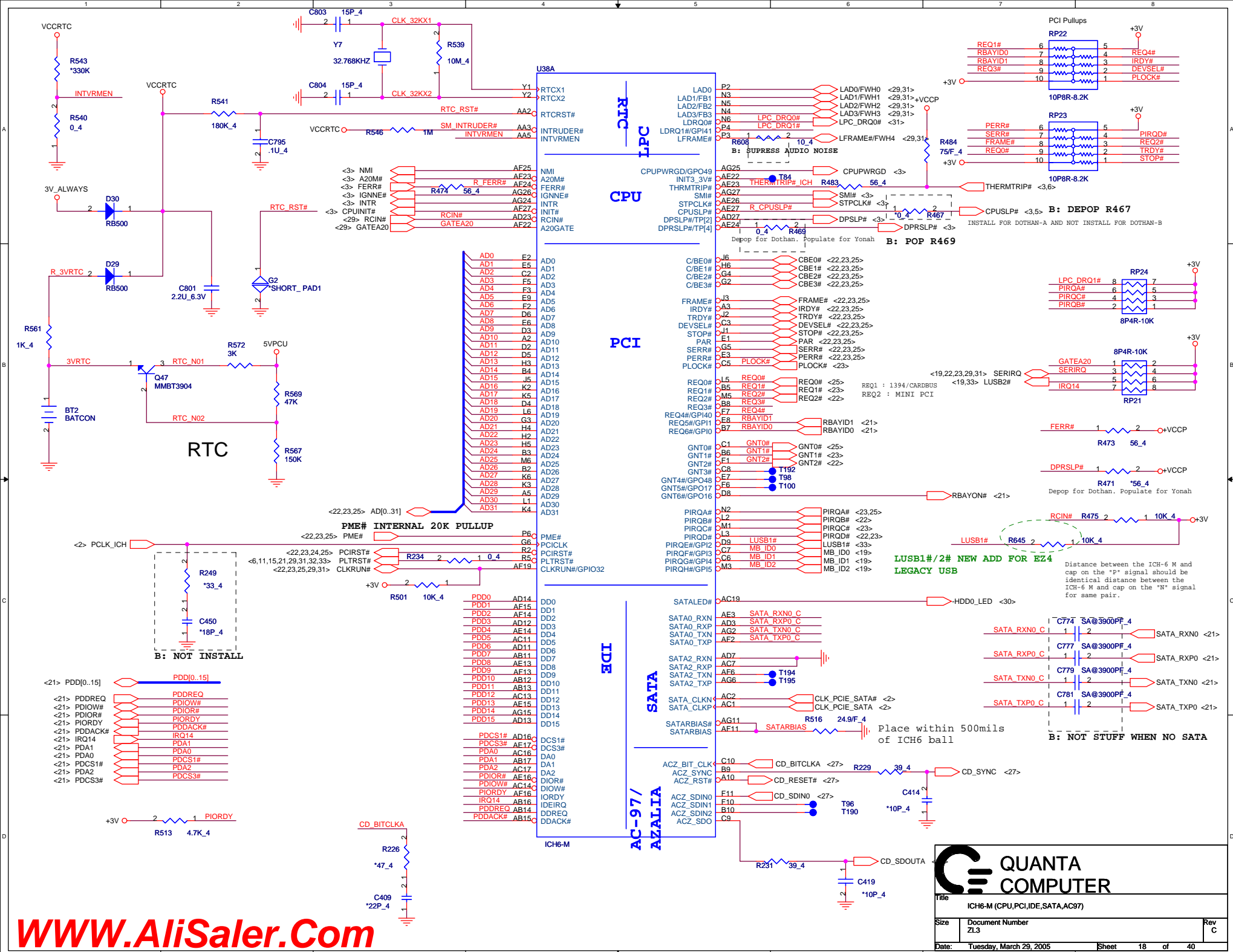


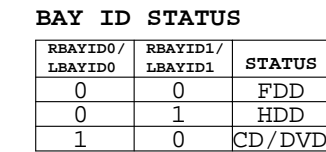
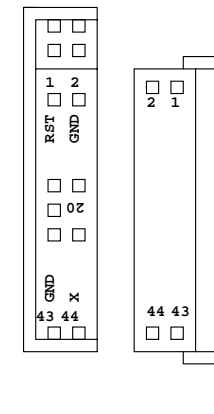
C: Change to FDV301N for Vgs issue.

CHANGE TO 0ohm for Acer LCD

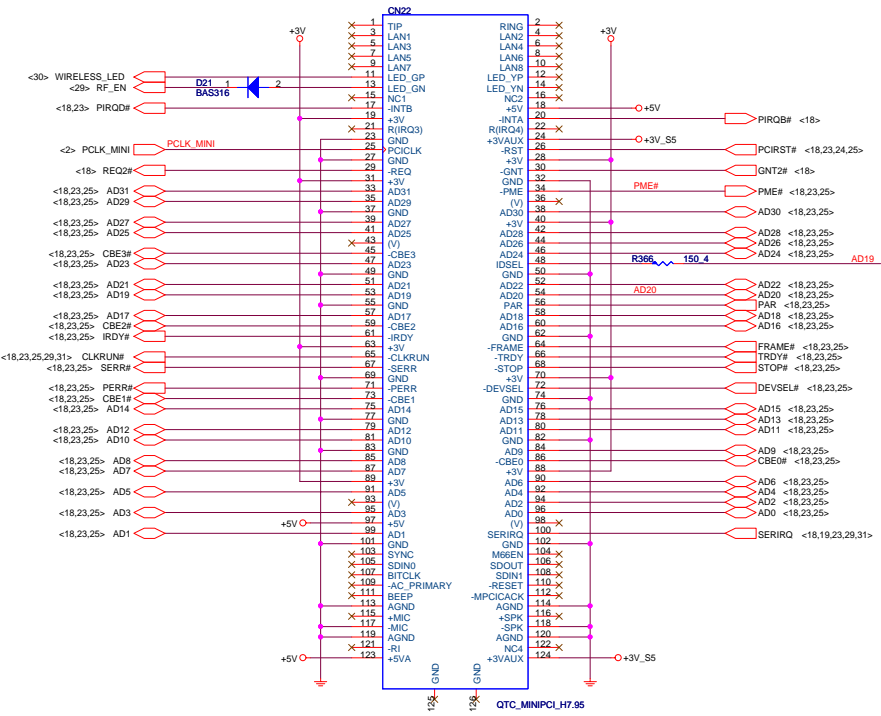


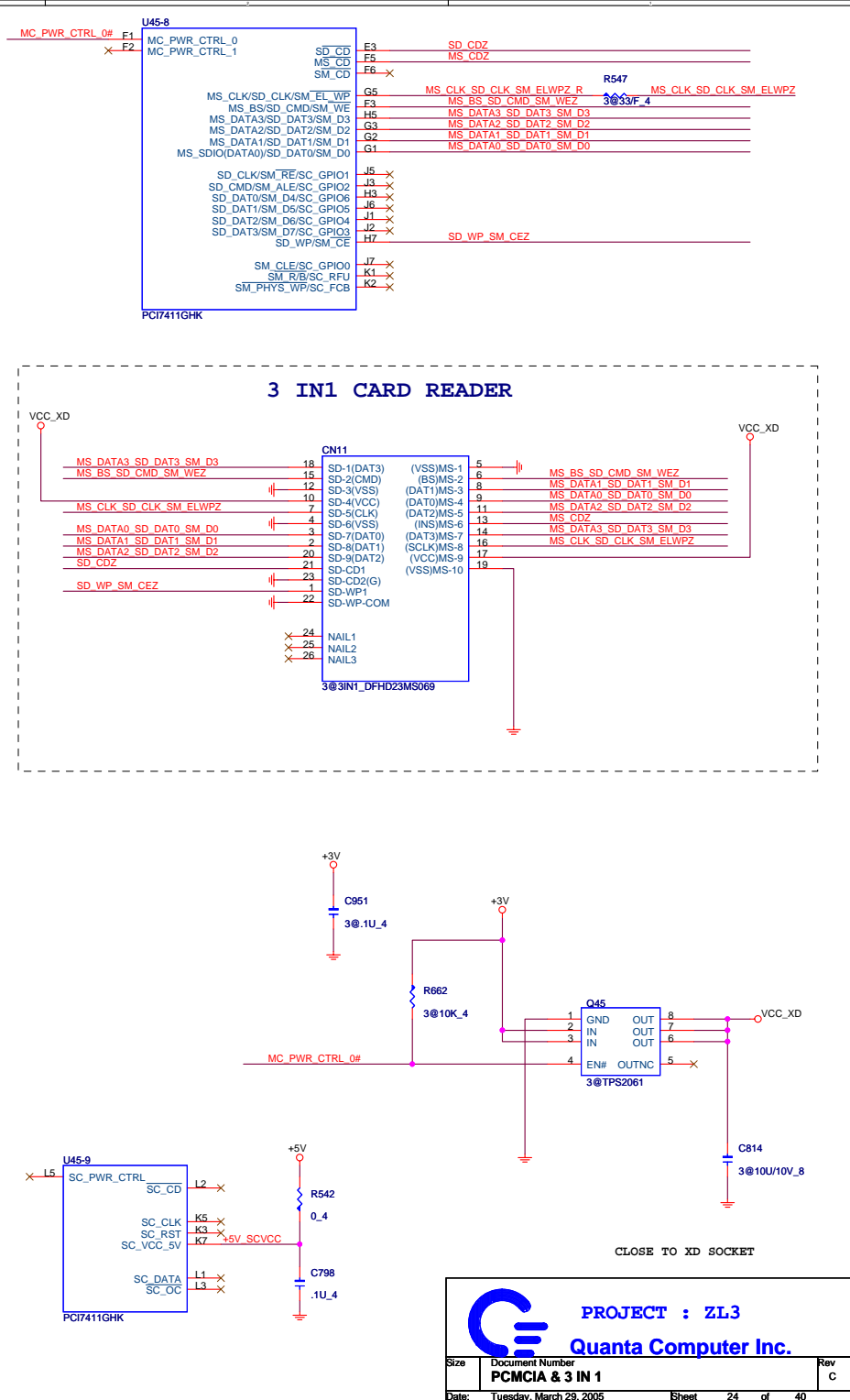
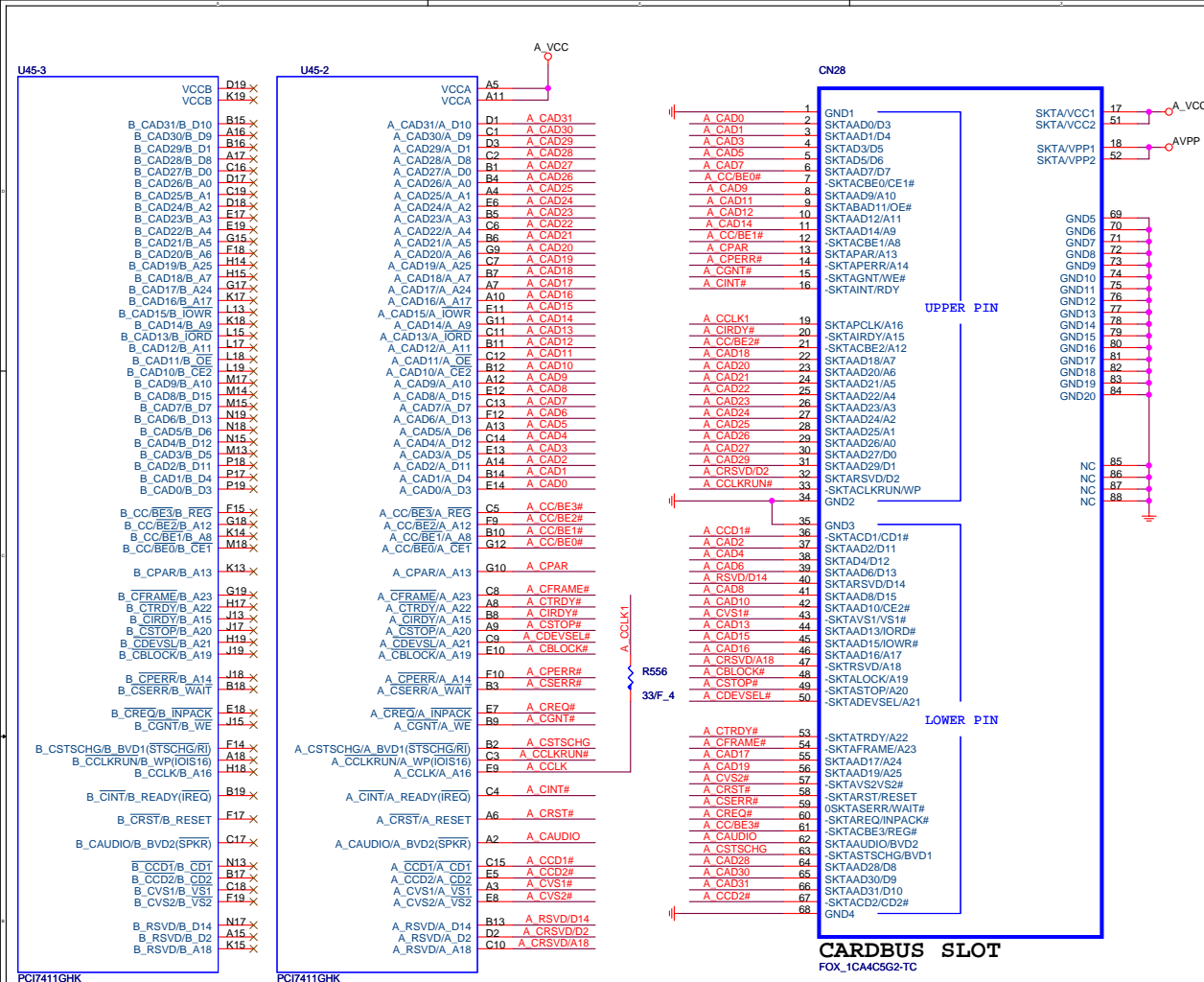
PROJECT : ZL3
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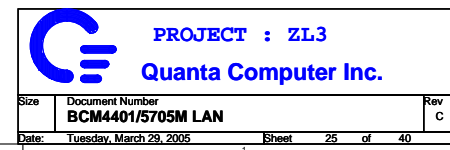


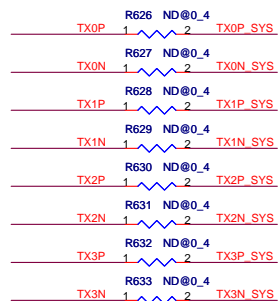
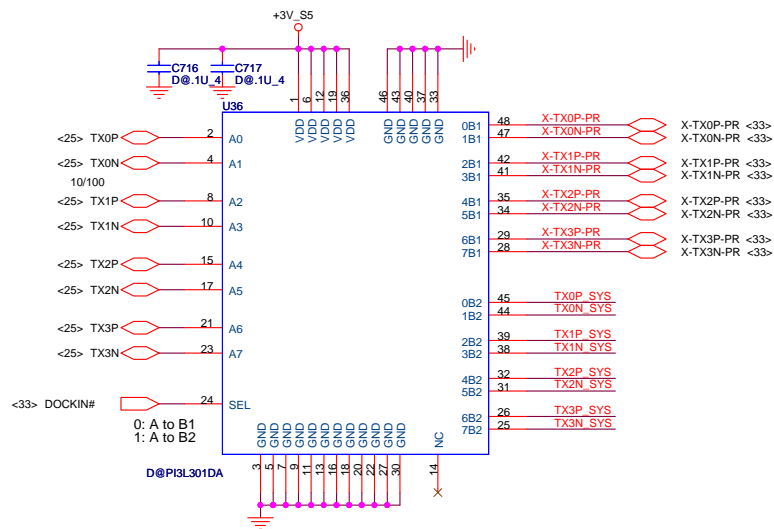


MINI-PCI

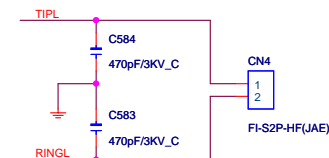
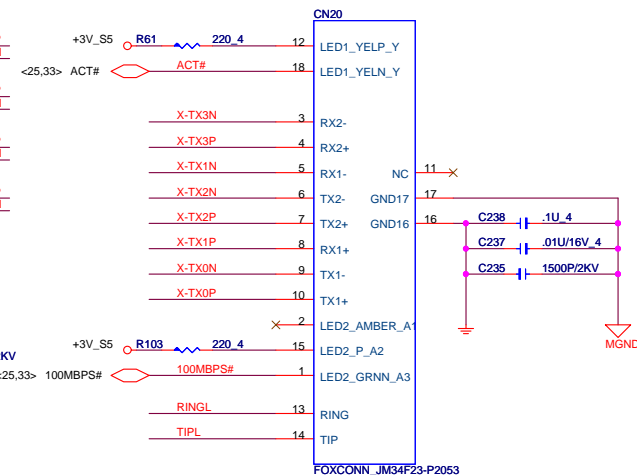
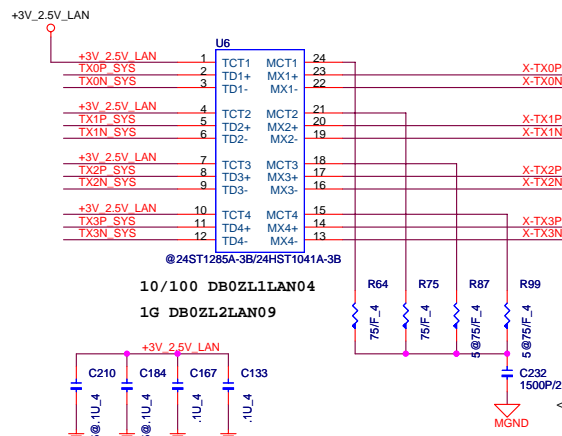






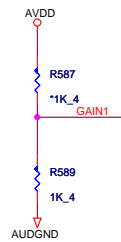


ADD CIRCUITS WHEN NO DOCKING

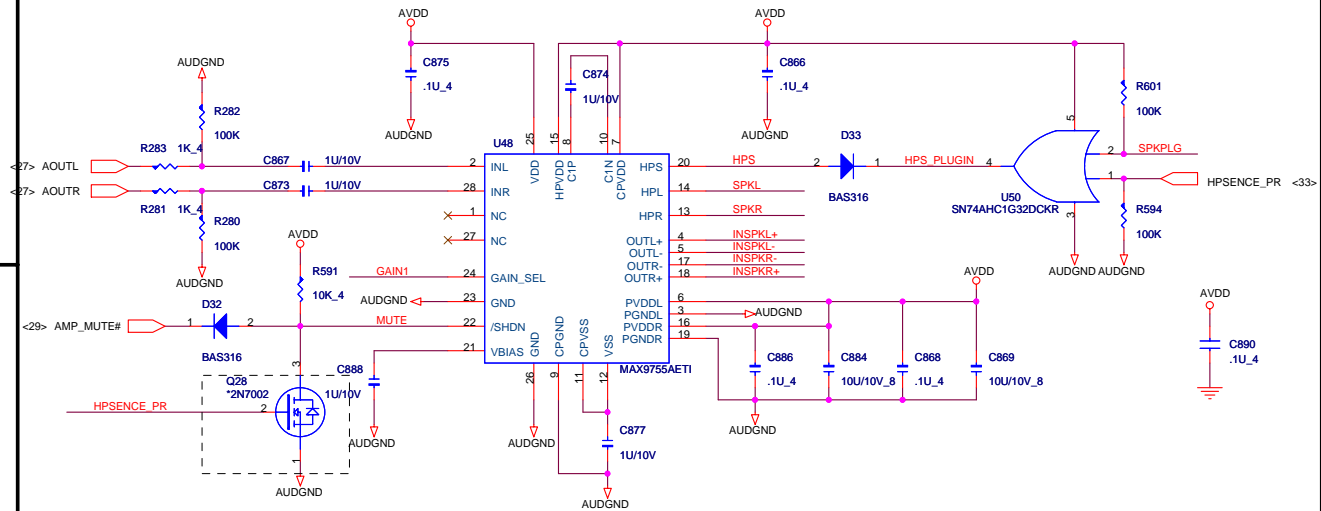
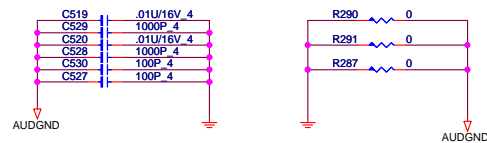
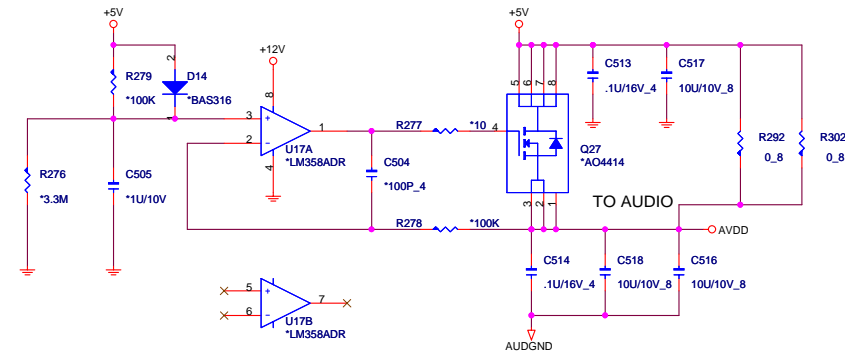


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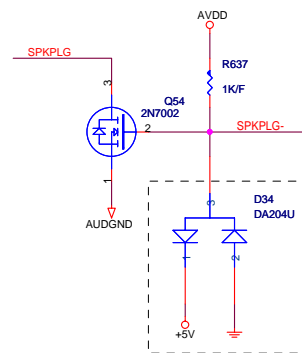
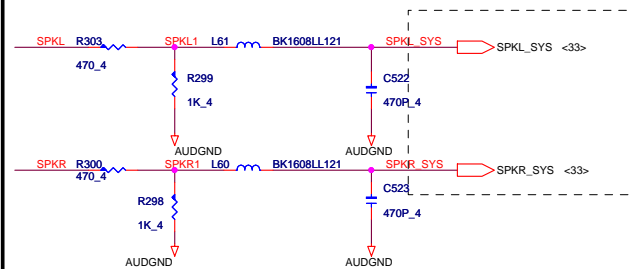
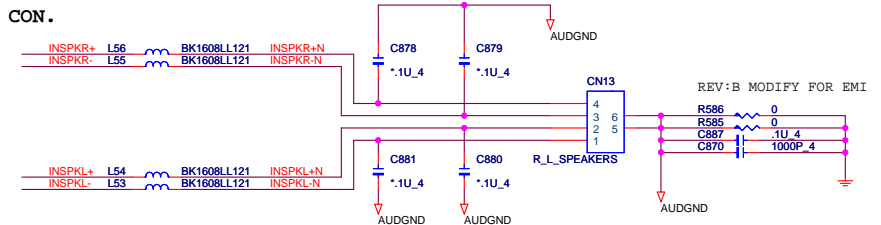
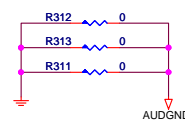
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



AMP POWER

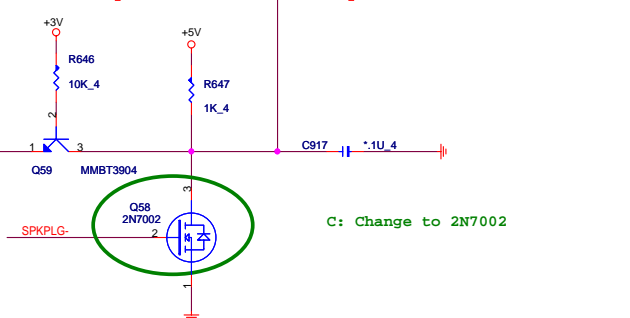
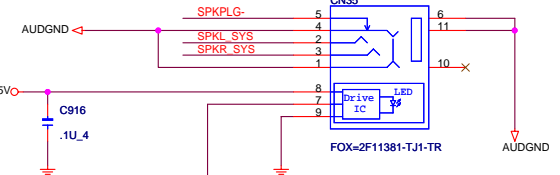


SPEAKER CON.



F: NEW ADD FOR ESD
CLOSE TO CN35

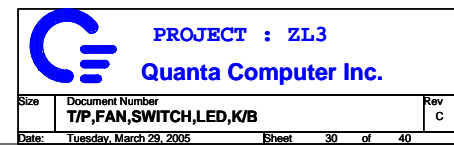
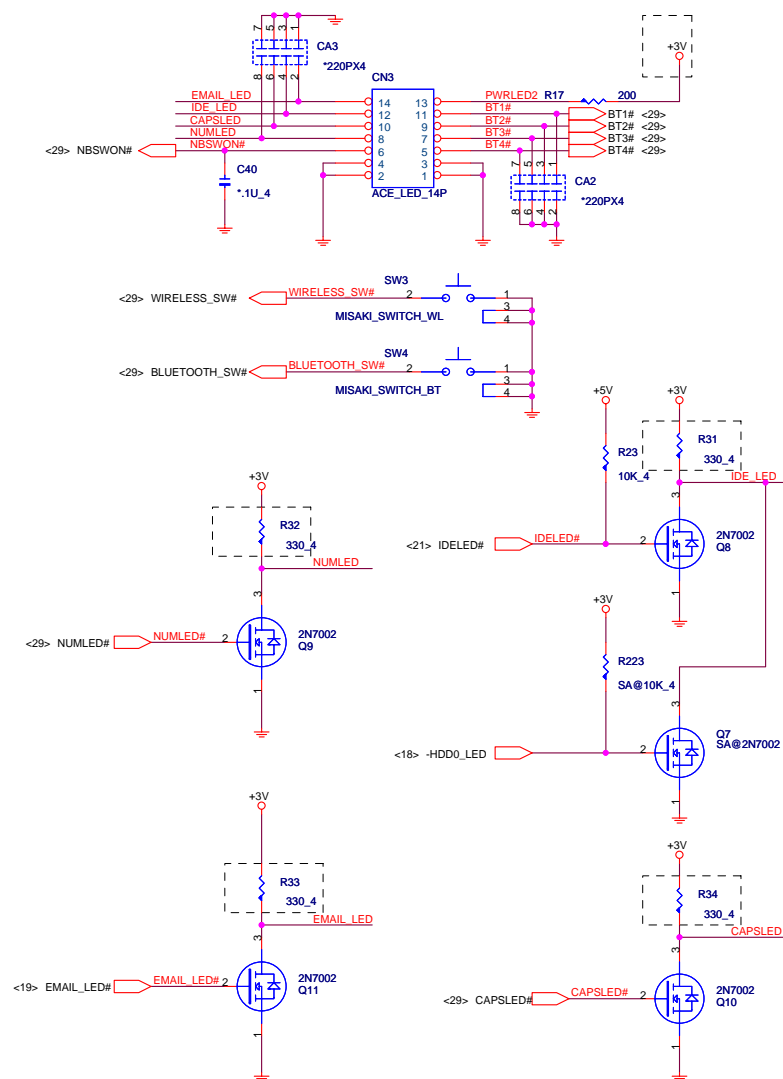
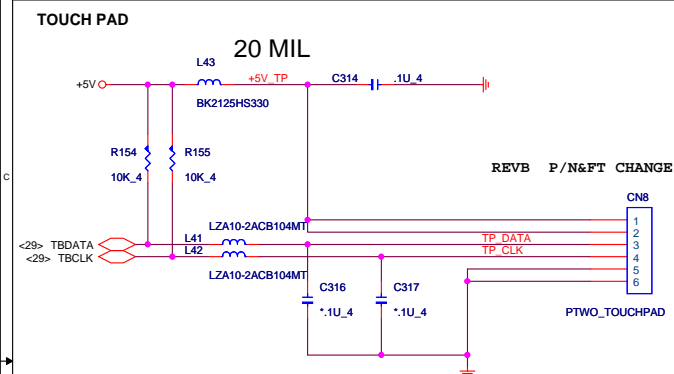
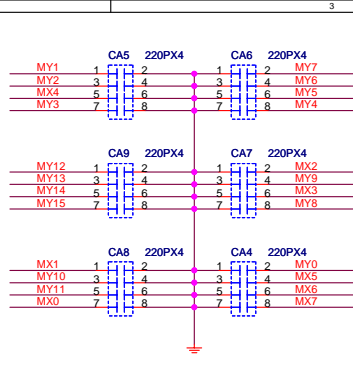
LINE OUT&SPDIF

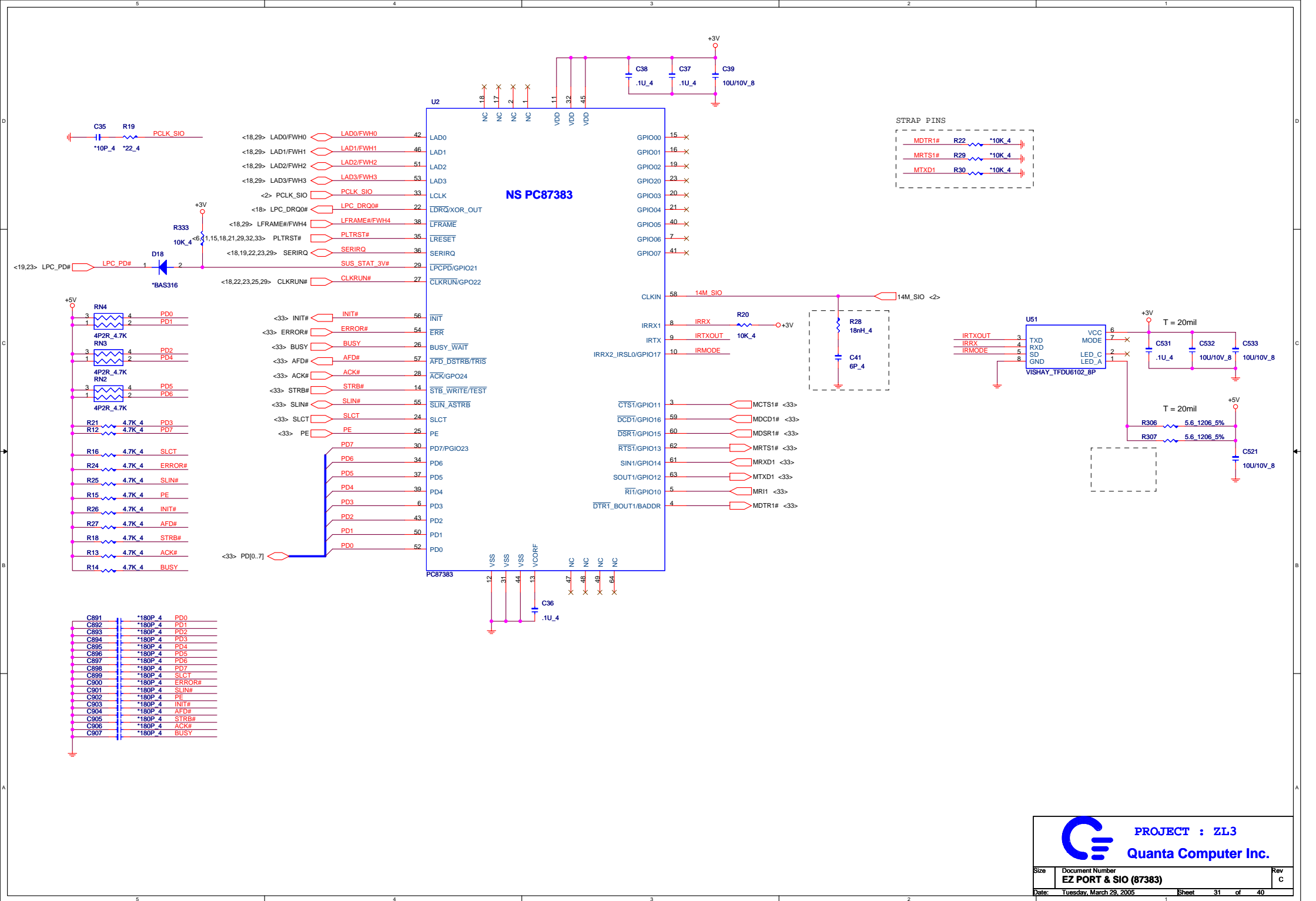


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Quanta Computer Inc.

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AUDIO AMP
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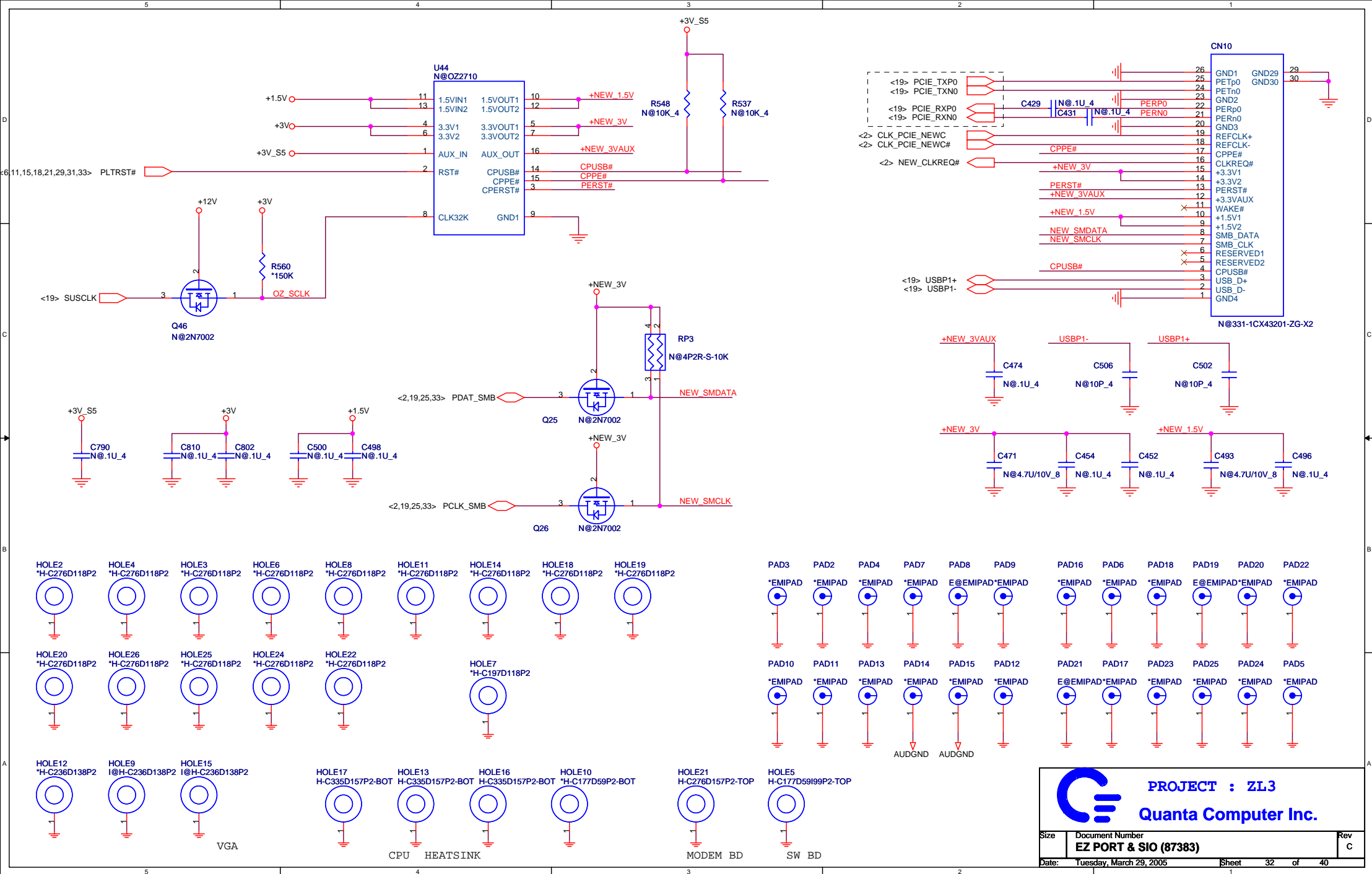


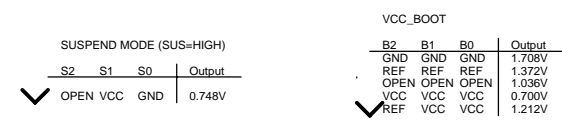





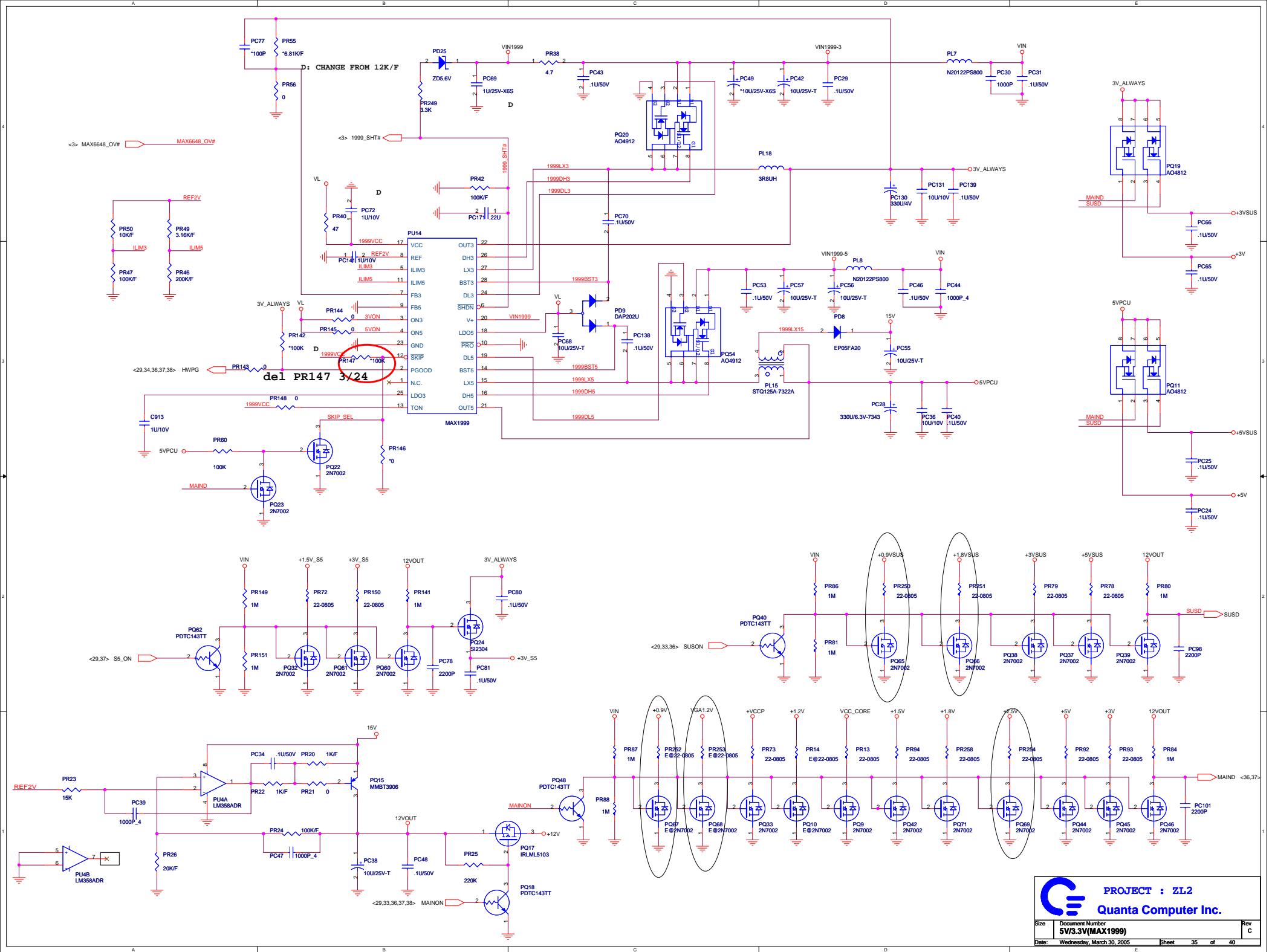
PROJECT : ZL3
Quanta Computer Inc.

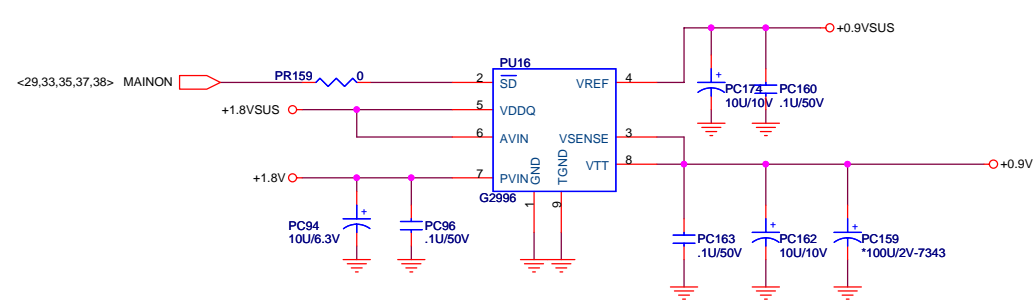
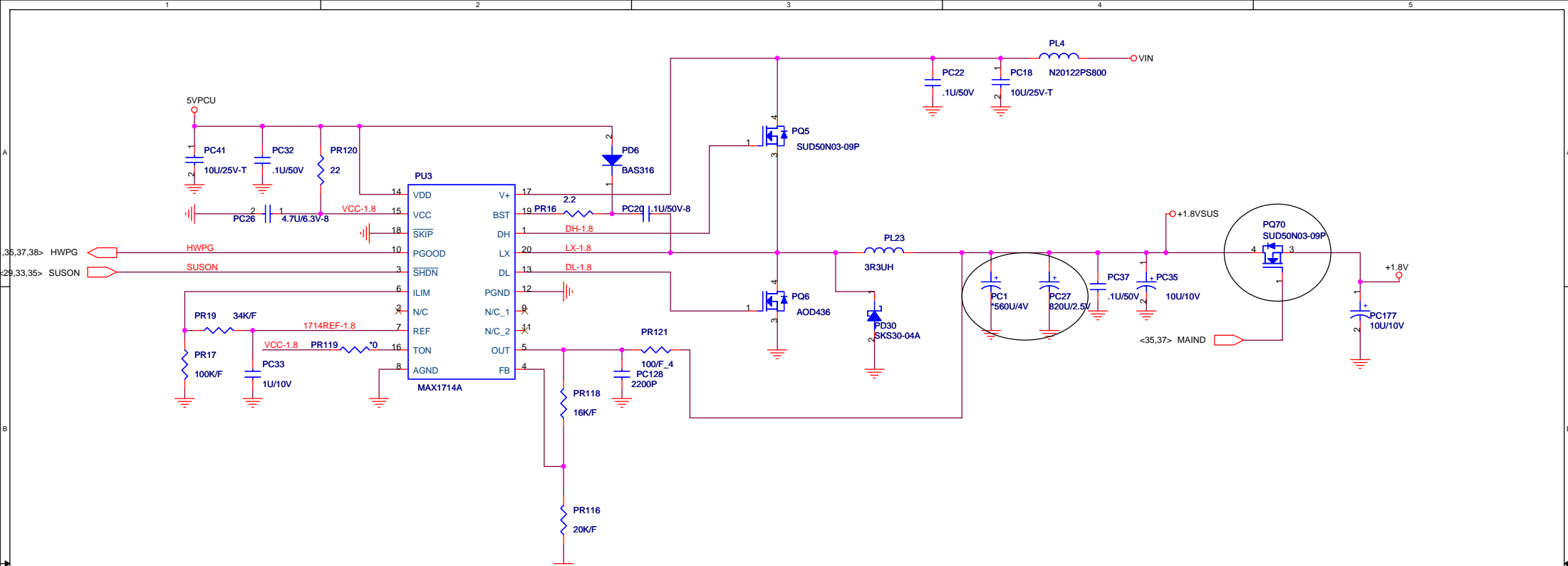
Size	Document Number	Rev
	EZ PORT & SIO (87383)	C
Date:	Tuesday, March 29, 2005	Sheet 31 of 40

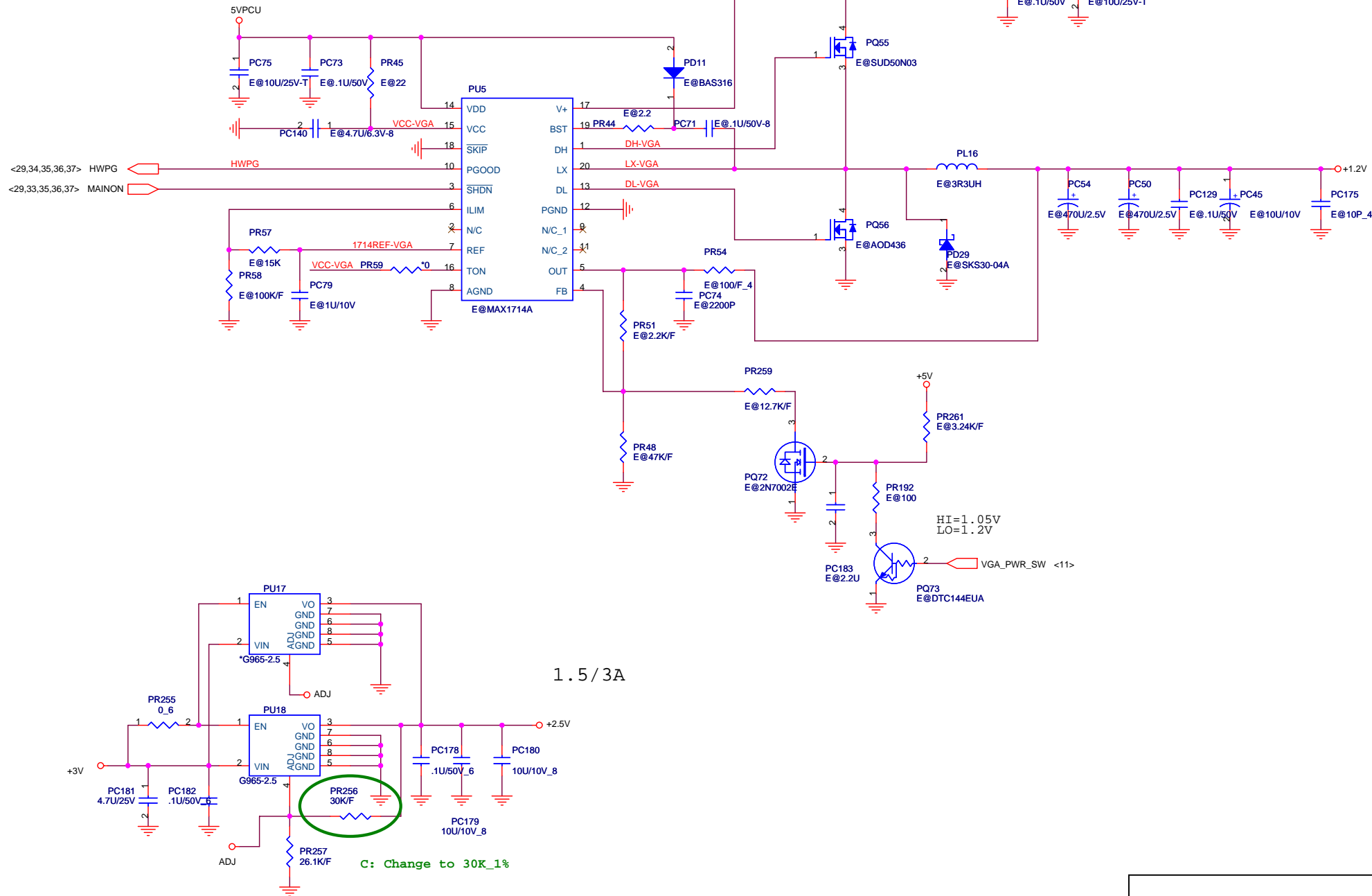


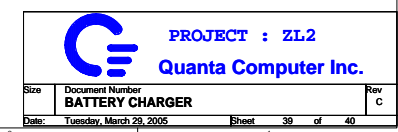


	PROJECT : ZL2	
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Size	Document Number	Rev
	CPU CORE (MAX1907)	C
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	Sheet	34 of 40

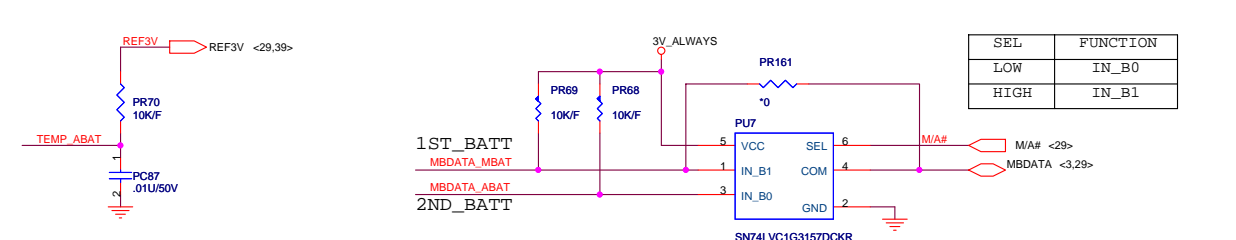
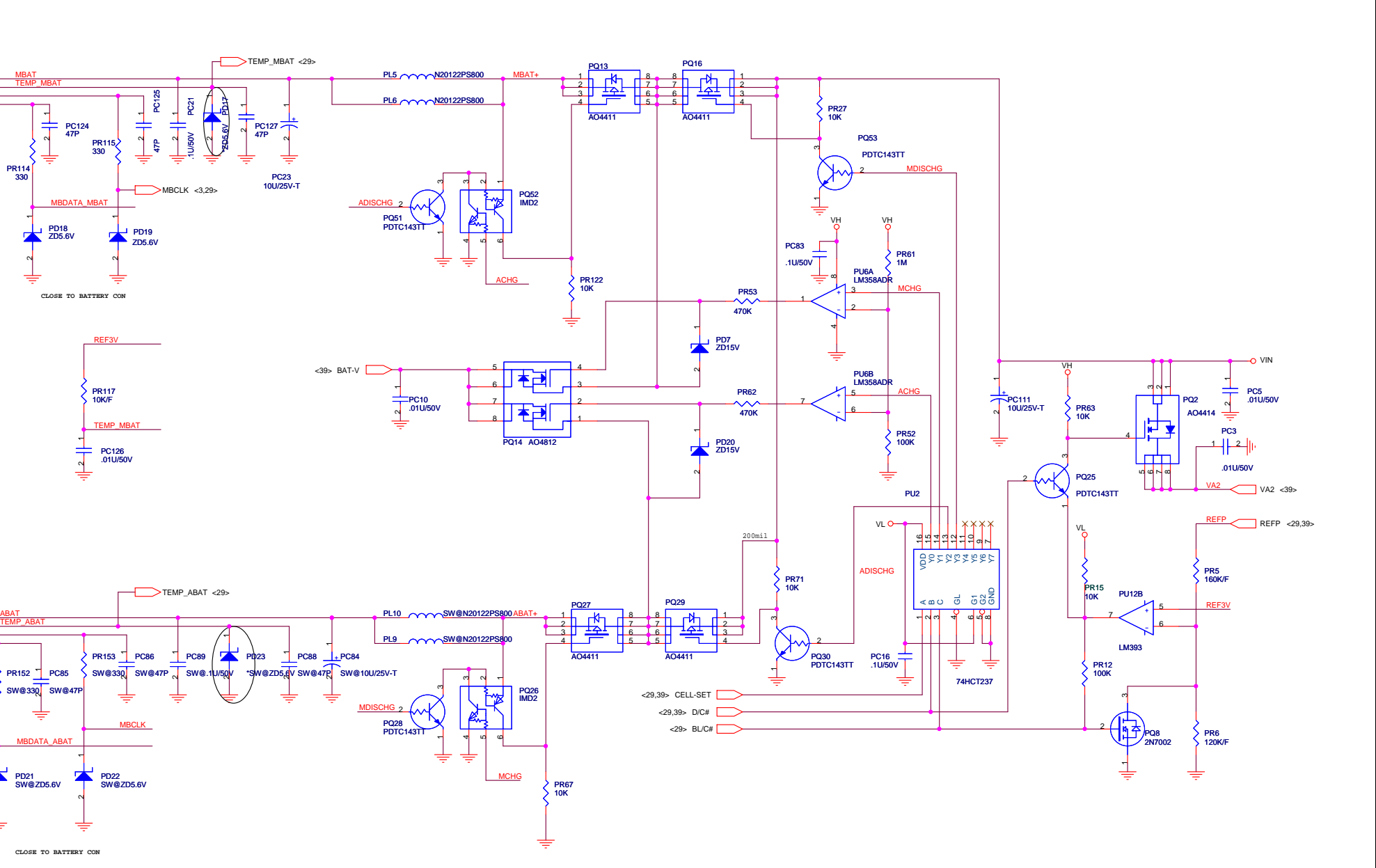








1	2	3	4	5
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SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



Size	Document Number BATTERY SELECT	Rev C
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